JTO 深圳市晶腾光电有限公司 SHENZHEN JINGTENG OPTOELECCTRONICS CO., LTD. **Product Specification For LCD Module CUSTOMER No: IP355** Model NO: JTO13164403001A **REVISION: 2.0 APPROVAL FOR SPECIFICATIONS ONLY** APPROVAL FOR SPECIFICTAIONS AND SAMPLE APPROVED BY **CUSTOMER** DIRECTOR QA R&D JTOLCM R&D CENTER **APPROVED BY** CHECKED BY PREPARED BY ZHANG MARKE GYD DIRECTOR MANAGER Electronic Mechanism 深圳市晶腾光电有限公司 SHENZHEN JINGTENG OPTOELECTRONICS Co., LTD. 深圳市龙华新区观澜南大富社区环观中路 308 号 E 区 601B 6F.601,E-Qu,No.308,HuanGuanZhongRoad,GuanLanNanDaFuShe-Qu, New LongHua District, Shenzhen, China TEL: +86-0-18926789425 FAX: +86-755-23272526

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| REV | COMMENT | PAGE | DATE |
|-----|-----------------|------|-----------|
| 1.0 | Initial Release | 1-20 | 2014-4-17 |
| 2.0 | revise | 1-21 | 2014-5-20 |
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| GENERAL SPECIFICAT | ION |
|--------------------|---|
| ITEM | CONTENTS |
| Module Size | $95.2(W) \times 42.0(H) \times 5.0(T) mm$ |
| Display Format | 131 × 64 DOTS |
| View Area | 63.46 (W) × 23.0(H) mm |
| Dot Size | 0.38mm* 0.32 mm |
| Dot Pitch | 0.42mm * 0.36mm |
| LCD Type | FSTN / POSITIVE / TRANSFLECTIVE |
| View Angle | 6 'clock |
| Controller IC | ST7565R |
| Duty Ratio | 1/65 Duty |
| Bias | 1/9 Bias |
| Connector | COG |

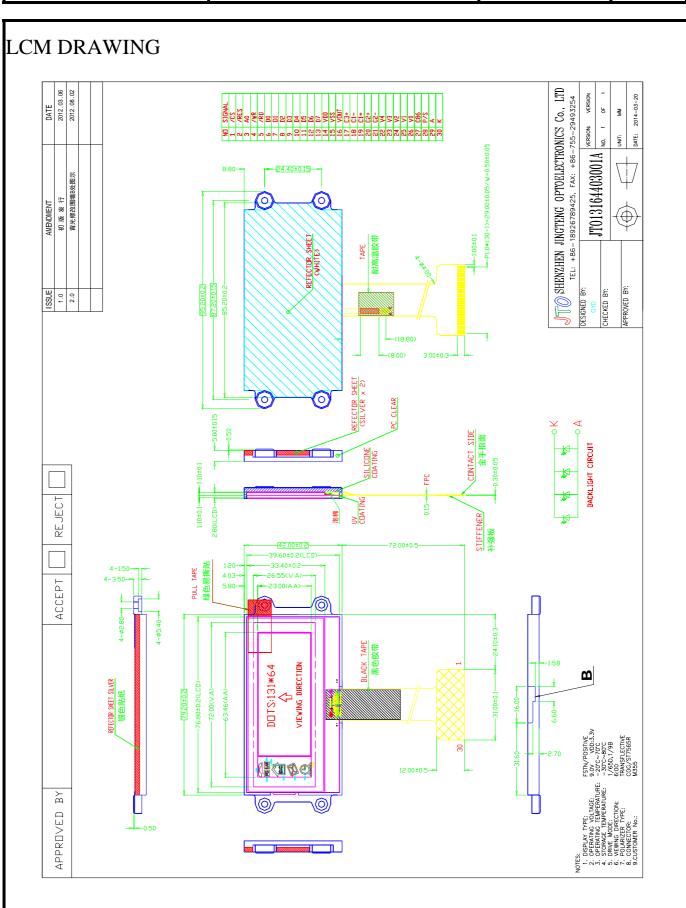


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| ABSOLUTE MAXIMUM RATING | | | | | | | | | |
|-------------------------|------------------|------|------|------|------|----------|--|--|--|
| Item | Symbol | Min. | Туре | Max. | Unit | Humidity | | | |
| Power Supply Voltage | V _{DD} | 0.3 | | 3.6 | Volt | | | | |
| Power Supply for LCD | Vop | 8.8 | 9.0 | 9.2 | Volt | | | | |
| Power Supply Voltage | V _{OUT} | -0.3 | - | 14.5 | Volt | | | | |
| Operating Temperature | Тор | -20 | - | +70 | | Note1 | | | |
| Storage Temperature | Tst | -30 | - | +80 | | Note2 | | | |

Note1: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Ta 70 : 75%RH max

Ta>70 : absolute humidity must be lower than the humidity of 75%RH at 70

Note2: Ta at -30 will be <48hrs, at 80 will be <120hrs when humidity is higher than 75%RH.

Ta 80 : 75%RH max

Ta>80 : absolute humidity must be lower than the humidity of 75%RH at 70

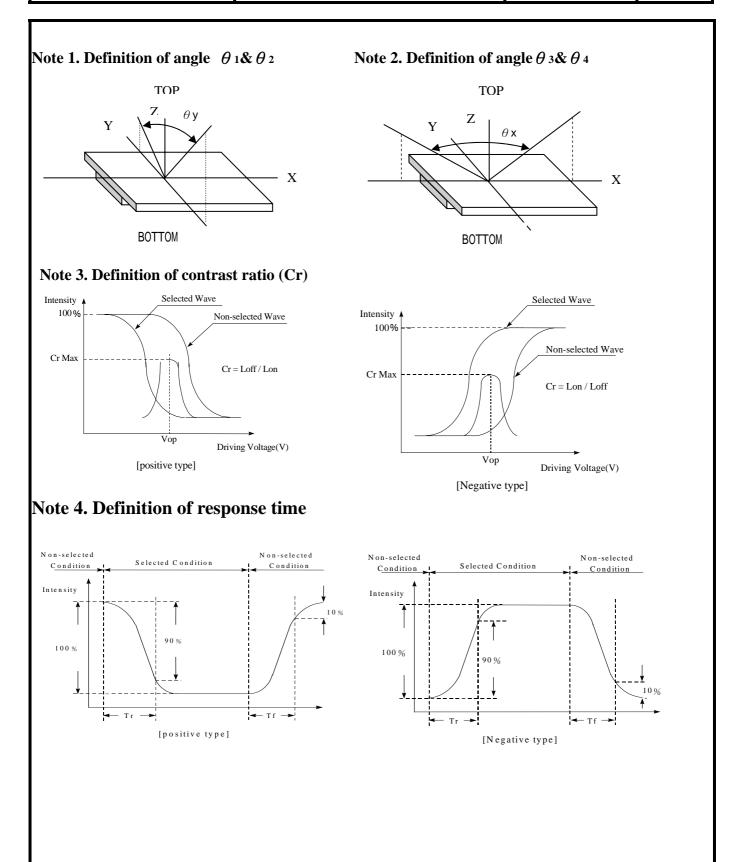


| OPTICAL CHARACTERISTICS | | | | | | | | | |
|-------------------------|----------------------|------------|------|------|------|------|-----------|------|--|
| lte | m | Symbol | Min. | Тур. | Max. | Unit | Condition | Note | |
| | $\phi = 0^{\circ}$ | θ 1 | | | 30 | | | | |
| Viewing | $\phi = 180^{\circ}$ | θ2 | | | 10 | deg. | T of O | 4.0 | |
| Angle Cr ≧2 | ϕ =90° | θ3 | | | 25 | | T=25°C | 1.2 | |
| | ϕ =270° | θ4 | | | 25 | | | | |
| Cont Ra | | Cr | | 7 | | | T=25°C | 3 | |
| Response Time (rise) | | Tr | 80 | | 200 | ms | T=25°C | 4 | |
| Response Time (fall) | | Tf | 80 | | 200 | ms | T=25°C | 4 | |

BACKLIGHT

| PARAMETER | Sym. | Min. | Тур. | Max. | Unit | Test Condition | Note |
|------------------------|------|------|------|------|-------------------|-------------------|------|
| Supply Current | I | - | 60 | 80 | mA | | |
| Supply Voltage | V | 3.0 | 3.2 | - | V | - | |
| Luminous Intensity | IV | 150 | | - | Cd/m ² | | |
| Uniformity | - | 80 | | - | % | | |
| | Х | 0.24 | | 0.31 | nm | | |
| Chromaticty Coordinate | Y | 0.24 | | 0.31 | nm | | |
| Color White | | | | | | | |

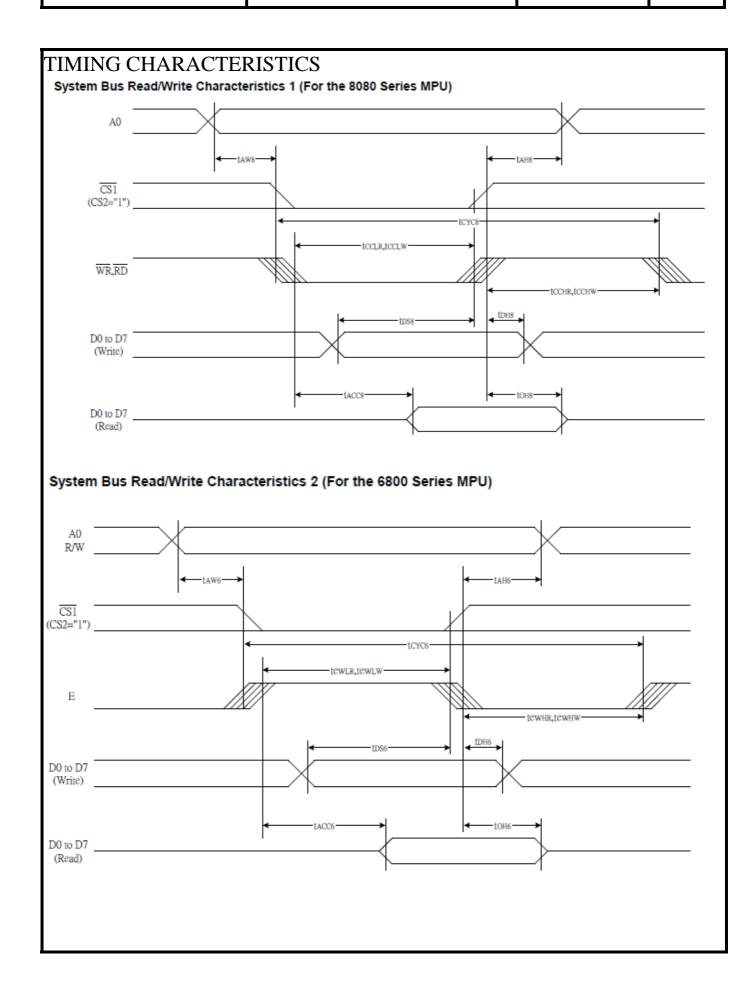






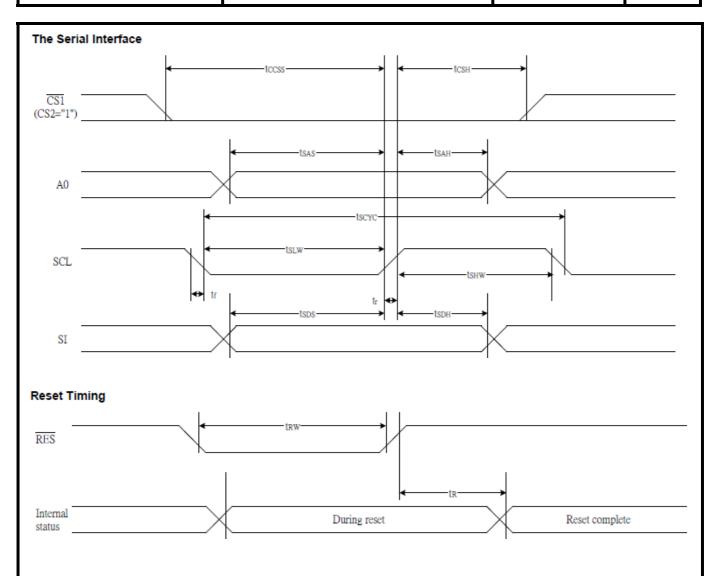
| PIN | SYMBOL | FUNCTIONS |
|-----|--------|---|
| 1 | /CS | This is the chip select signal |
| 2 | /RES | settings are initialized |
| 3 | A0 | "H ": Indicates that D0 to D7 are display data. "L": Indicates that D0 to D7 are control data |
| 4 | /WR | When R/W = "H": Read. When R/W = "L": Write |
| 5 | /RD | 8080 6800 |
| 6 | D0 | This is an 8-bit |
| 7 | D1 | This is an 8-bit |
| 8 | D2 | This is an 8-bit |
| 9 | D3 | This is an 8-bit |
| 10 | D4 | This is an 8-bit |
| 11 | D5 | This is an 8-bit |
| 12 | D6 | This is an 8-bit |
| 13 | D7 | This is an 8-bit |
| 14 | VDD | Power supply |
| 15 | VSS | Ground |
| 16 | VOUT | DC/DC voltage converter |
| 17 | C3+ | DC/DC voltage converter |
| 18 | C1- | DC/DC voltage converter |
| 19 | C1+ | DC/DC voltage converter |
| 20 | C2+ | DC/DC voltage converter |
| 21 | C2- | DC/DC voltage converter |
| 22 | V4 | V0 V1 V2 V3 V4 Vss |
| 23 | V3 | V0 V1 V2 V3 V4 Vss |
| 24 | V2 | V0 V1 V2 V3 V4 Vss |
| 25 | V1 | V0 V1 V2 V3 V4 Vss |
| 26 | V0 | V0 V1 V2 V3 V4 Vss |
| 27 | C86 | "H ": 6800 Series MPU interface. C86 = "L": 8080 MPU interface |
| 28 | PS | P/S = "H": Parallel data input. P/S = "L": Serial data input |
| 29 | А | LED+ |
| 30 | К | LED- |

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DC CHARACTERISTICS

| | | Cumahaal | | ndition | | Rating | | Units | Applicable |
|---------------|--------------------------|----------|-------------------------|--------------------------------|-----------|--------|-----------|-------|------------|
| Ite | em | Symbol | Condition | | Min. | Тур. | Max. | Units | Pin |
| Operating | Voltage (1) | VDD | | | 1.8 | _ | 3.3 | v | Vss*1 |
| Operating | Voltage (2) | VDD2 | (Relative f | to Vss) | 2.4 | _ | 3.3 | v | Vss |
| High-level Ir | nput Voltage | VIHC | | | 0.8 x VDD | _ | VDD | V | *3 |
| Low-level In | iput Voltage | VILC | | | Vss | _ | 0.2 x VDD | V | *3 |
| High-level O | utput Voltage | VOHC | юн =0.5 | i mA | 0.8 x VDD | _ | VDD | V | *4 |
| Low-level Ou | Low-level Output Voltage | | Iol = 0.5 mA | | Vss | _ | 0.2 x VDD | V | *4 |
| Input leaka | Input leakage current | | VIN = VDD or VSS | | -1.0 | _ | 1.0 | μA | *5 |
| Output leak | age current | ILO | VIN = VDD or Vss | | -3.0 | _ | 3.0 | μA | *6 |
| Liquid Cryst | al Driver ON | | Ta = 25°C | Vo = 13.0 V | _ | 2.0 | 3.5 | KO | SEGn |
| | tance | RON | (Relative To VDD) | Vo = 8.0 V | _ | 3.2 | 5.4 | KΩ | COMn *7 |
| Static Consun | nption Current | Issq | Vo = 13.0 V(Relative To | | _ | 0.01 | 2 | μA | VDD, VDD2 |
| Output Leak | age Current | 15Q | VDD) | | _ | 0.01 | 10 | μA | V0 |
| Input Termina | I Capacitance | CIN | Ta = 25°C | ; f = 1 MHz | _ | 5.0 | 8.0 | pF | |
| | Internal Oscillator | fosc | 1/65 duty | To - 0510 | 17 | 20 | 24 | kHz | *8 |
| Oscillator | External Input | fcL | 1/33 duty | Ta = 25°C | 17 | 20 | 24 | kHz | CL |
| Frequency | Internal Oscillator | fosc | 1/49 duty | To - 05%0 | 25 | 30 | 35 | kHz | *8 |
| | External Input | fcL | 1/53 duty 1/55 duty | /53 duty Ta = 25°C /55 duty | 25 | 30 | 35 | kHz | CL |

| | Item | Symbol | Symbol Condition | | Rating | Units | Applicable | |
|----------|---|--------|--|------|--------|-------|------------|------|
| | nem | Symbol | Condition | Min. | Тур. | Max. | Units | Pin |
| | Input voltage | VDD2 | (Relative To Vss) | 2.4 | — | 3.3 | V | Vss |
| | Supply Step-up output voltage Circuit | | (Relative To Vss) | _ | _ | 16.0 | V | Vout |
| al Power | Voltage regulator Circuit Operating Voltage | Vout | (Relative To Vss) | 6.0 | _ | 16.0 | v | Vout |
| Internal | Voltage Follower Circuit Operating Voltage | V0 | (Relative To Vss) | 4.0 | _ | 13.0 | v | V0*9 |
| | Base Voltage | VR | Ta = 25°C , (Relative To Vss) –0.05%/°C | 2.07 | 2.10 | 2.13 | v | *10 |



PAGE

| | No | Test Item | Content of Test | Test Condition |
|------------------|----|--------------------------------------|--|-------------------------------|
| | 1 | High Temperature Storage | Endurance test of high temperature for a long time. | 80 ± 2 48Hrs |
| | 2 | Low Temperature Storage | Endurance test of low temperature for a long time. | -30 ± 2 48Hrs |
| est | 3 | High Temperature Operation | Endurance test of electrical stress (Voltage & Current) and the thermal stress to the element. | 70 ± 2 48Hrs |
| Environment Test | 4 | Low Temperature Operation | Endurance test of electrical stress (Voltage & Current) and the thermal stress to the element | -20 ± 2 48Hrs |
| En | 5 | High Temperature Humidity Storage | Endurance Test of high temperature and high humidity for a long time. | 40 ± 2 70 ± 2% RH 48Hrs |
| | 6 | Thermal shock | Endurance test of low and high temperature cycles.(air to air) $-10 \pm 2 \iff 50 \pm 2$ (60min) \iff (60min) | 10 cycle |

Note: 1) When making the low temperature test, not to dewy.

2) Driving condition for operation test.

Power Supply Voltage for Logic System (VDD) = 3.3V

Failure Judgment Criterion

After the above mentioned test.

(For Environmental Test, after 2 hours in room temperature.)

There should not be conspicuous failure of display quality and appearance.

2) Contrast ratio should be 50% of the initial contrast ratio.

3) There should not have any abnormality of functions.



SHIPMENT STANDARD

1、The Voltage Standard of Mass Production & Shipments: Vop should between $_+$ V ~ ____V (With an allowance of ±0.2V). If not acceptable, please inform the sales from SHENZHEN JINGTENG OPTOELECTRONICS Co., LTD.

量产出货时电压标准:Vop控制在承认样品的 _+ __V ~ _- _V之间,(空白表明接受公司标准:±0.2V) 若有异议,请向深圳市**晶腾**光电有限公司的业务人员提出。

2、For product quality standard, please refer to "LCD Outgoing Quality Standard" and "Product Specifications". If there are any special requirements or disagreements, please inform the sales from SHENZHEN JINGTENG OPTOELECTRONICS Co., LTD.

出货质量标准见深圳市**晶腾**光电有限公司的《LCM出货标准》或《产品规格书》若对出货标准有异议或有特别 要求,请向深圳市蓝龙光电有限公司的业务人员提出。

PACKAGING MODE

普通包装/ Regular Packaging

中性包装/Neutral Packaging

内包装使用 "ROHS"标签/Symbolize the "RoHS" on the internal package

外包装使用 "ROHS"标签/Symbolize the "RoHS" on the external package



INSPECTION CRITERIA Defect Item Criterion for Defects Remark Туре Non Display Non Display is not allowed Major All Kinds of Line defects Such as Vertical, Horizontal, Cross Line Defect Major are not allowed Unnormally display unnormally is not allowed (data crack) Major display Leakage of liquid crystal is not acceptable Panel Major [Spot] Black Spot Size(mm) Acceptable Number White Spot Size: Φ≤0.15 2 Bright Spot Φ=(A+B)/2 Minor 1 $0.15 < \Phi \le 0.25$ Foreign Particle Pinhole В Distance ≥ 5mm Dimple

| Item | | | Defect Type | Remark | | | |
|-----------------------|--------------|---|----------------|---------------------|-------|---|--|
| [Line] | | * If we cannot see operating condition | | | | | |
| Black Line | - 1. L. 1 | Width(mm) | Length(mm) | Qty | | | |
| White Line Foreign | | W ≤ 0.03 | L≤2 | 2 | Minor | | |
| Particle Scratch | | | | $0.03 < W \le 0.05$ | 2<∟≤4 | 1 | |
| Scratch | | | | | | | |
| | <i>•</i> | Size(mm) | Acceptab | le Number | | | |
| Polarizer | Size: | Φ ≤ 0.15 | Ign | ore | | | |
| Defect | | 0.15 < Φ≤0.2 | 5 | 2 | Minor | | |
| . Dent | | $0.25 < \Phi \le 0.3$ | 0 | 1 | | | |
| . Bubble | | 0.3 < Φ | | D | | | |
| | | | | | | | |
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| Item | Criterion for Defects | | Defect Type | Remark |
|--------------|------------------------------------|--|-------------|--------|
| | | Progressive crack is not allowed | Major | |
| Glass Defect | c b Chipping [Pad area] | Size (mm) a ≤ 0.5 b: Ignore c ≤ t | | |
| | Chipping [Real of Pad area] | Size (mm) a ≤ 1.0 b: Ignore c ≤ t | Minor | |
| | Side Chipping [Except of Pad area] | Size (mm) a ≤ 1.0 b: Ignore c ≤ t | | |
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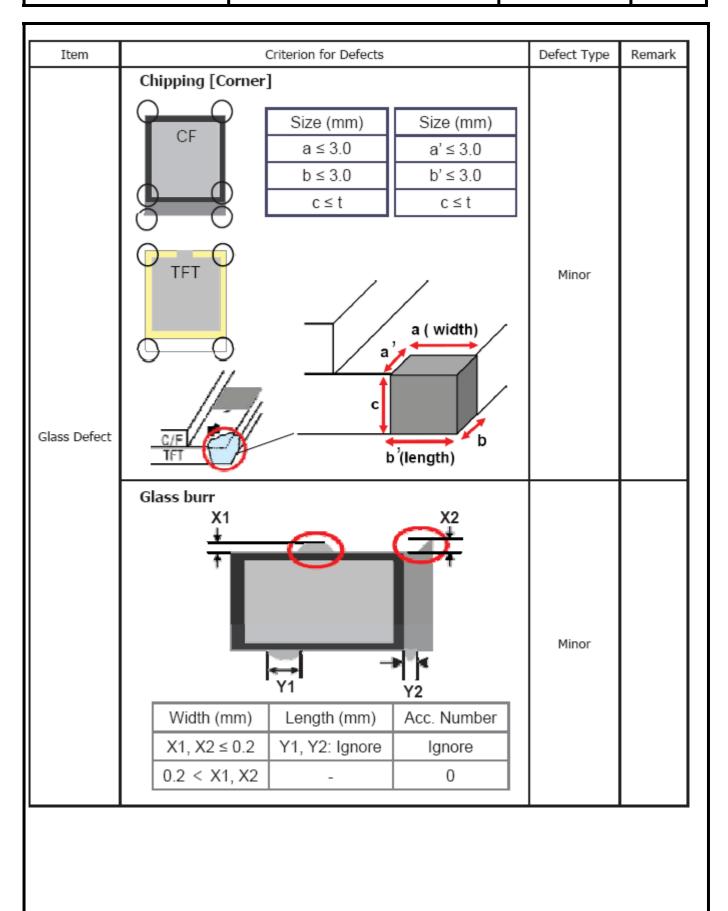


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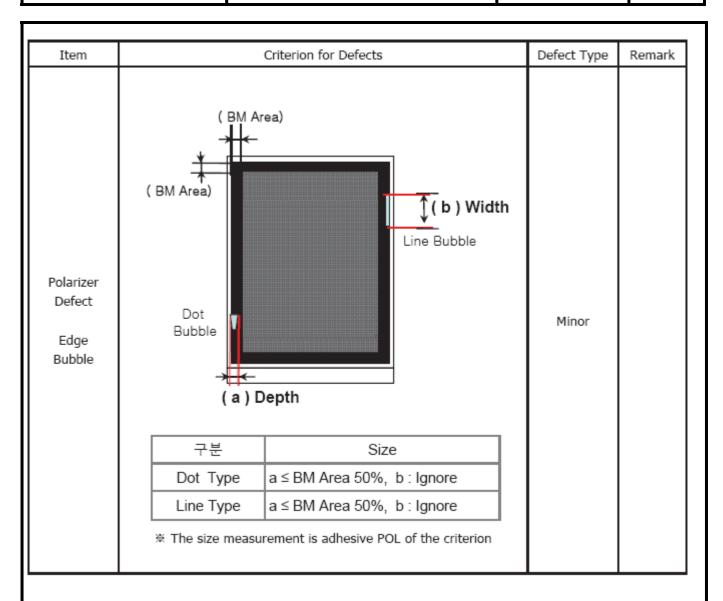
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Precaution for use of LCD module

- 11.1 Handling Precautions
- 1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 2) If the display panel is damaged and the liquid crystal substance inside it leaks out ,be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 5) If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:



- --Isopropyl alcohol
- --Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

--Water

--Ketone

--Aromatic solvents

- 6) Do not attempt to disassemble or process the LCD module.
- 11.2 Assembling Precautions
- When mounting the LCD module make sure that it is free of twisting, warping, and distortion. Distortion has great influence upon display quality. Also keep the stiffness enough regarding the outer case.
- 2) Please handle the LCD module by its side.
- 3) NC terminal should be open. Do not connect anything.
- 4) If the logic circuit power is OFF, do not apply the input signals.
- 5) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - •Be sure to ground the body when handling the LCD module.
 - ·Tools required for assembly, such as soldering irons, must be properly grounded.
 - •To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - •The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- 6) Be careful when treating the glass panel because it has very sharpened edge.

11.3 · Storage Precautions

- When storing the LCD module, avoid exposure to direct sunlight of to the light of fluorescent lamps and high temperature/high humidity. Whenever possible, the LCD module should be stored in the same conditions in which they were shipped from our company.
- 2) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets or a current flow in a high-humidity environment.

11.4 • Design Precautions

The absolute maximum ratings represent the rated value beyond which LCD module can not

- exceed. When the LCD modules are used in excess of this rated value, their operation characteristics may be adversely affected.
- 2) To prevent the occurrence of erroneous operation caused by noise, attention must be paid to satisfy



V_{IL}, V_{IH} specification values including taking the precaution of using signal cables that are short.

- 3) The LCD exhibits temperature dependency characteristics. Since recognition of the display becomes difficult when the LCD is used outside its designated operating temperature range, be sure to use the LCD within this range. Also keep in mind that the LCD driving voltage levels necessary for clear displays will vary according to temperature.
- 4) We recommended that power supply lines (VDD) have over-current protection line. (Fuse etc. Recommend Value:0.5A)
- 5) Sufficiently notice the mutual noise interference occurred by peripheral devices.
- 6) To cope with EMI, take measures basically on outputting side.
- 7) When installing an LCD module, fasten it at the LCD panel.
- 8) The display panel is made of general float glass which is not guaranteed for strength. So please consider about following.

•Do not subject panel to a mechanical shock by dropping directly.

·Do not let case to touch to panel directly.

Others

- Liquid crystal solidifies under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the LCD module is subjected to a strong shock at a low temperature.
- 2) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3) To minimize the performance degradation of the LCD module's resulting from destruction caused by static electricity, etc., exercise care to avoid touching the following section when handling this module: LCD's Terminal electrode sections.
- 4) Optimum voltage to obtain best contrast value depending on products. Therefore voltage adjustment with electric volume is required in each display.
- 5) Precaution for disposal of LCD module. When disposal of LCD module, ask specialization company of industrial waste which is permitted by the government. When burn up LCD module, obey the law of environmental hygienics. wash it off well with soap and water.

